

FIGURE 1

<u>210</u>	<u>210</u>	<u>210</u>	<u>210</u>	<u>210</u>
<u>216</u>				
<u>215</u>				



FIGURE 2

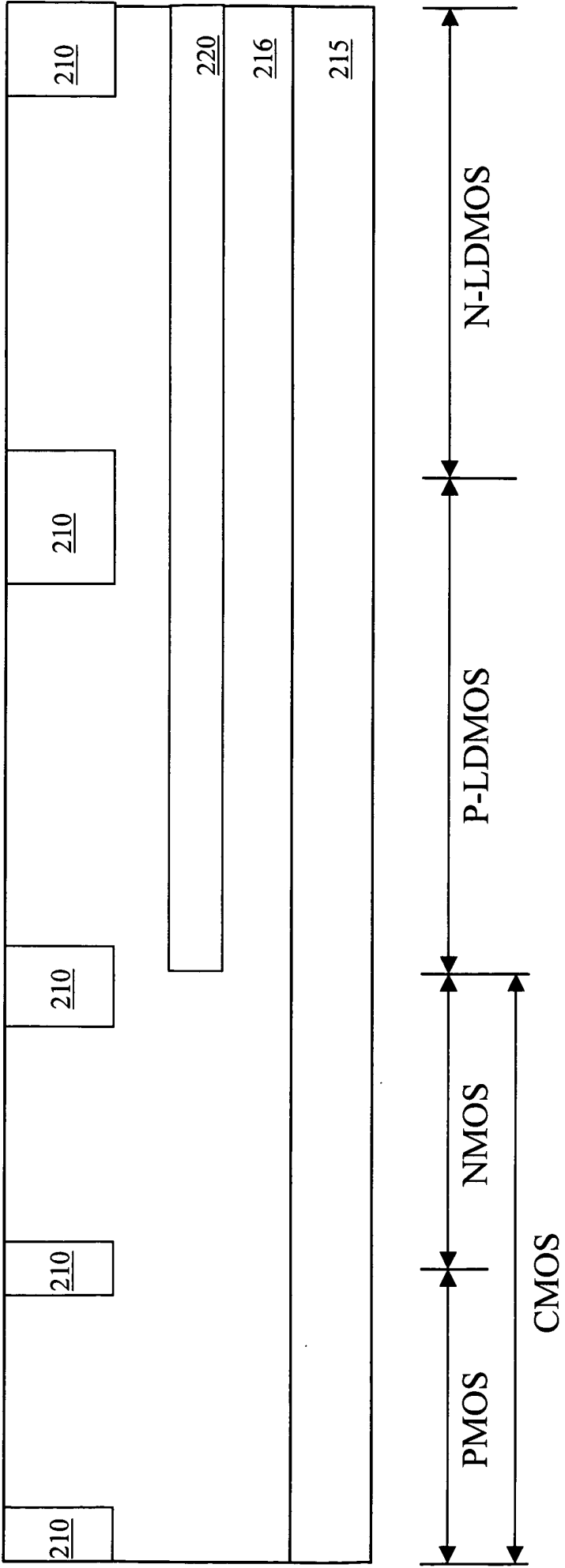


FIGURE 3

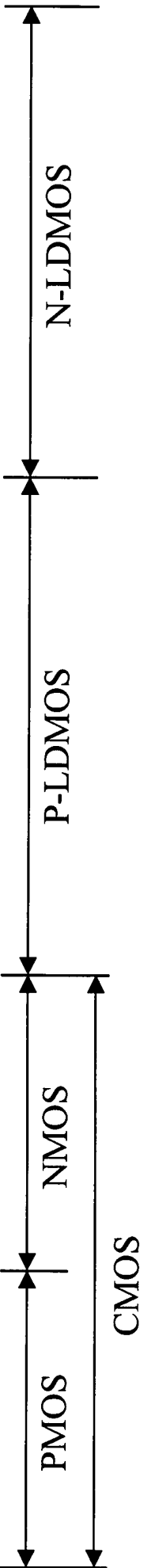
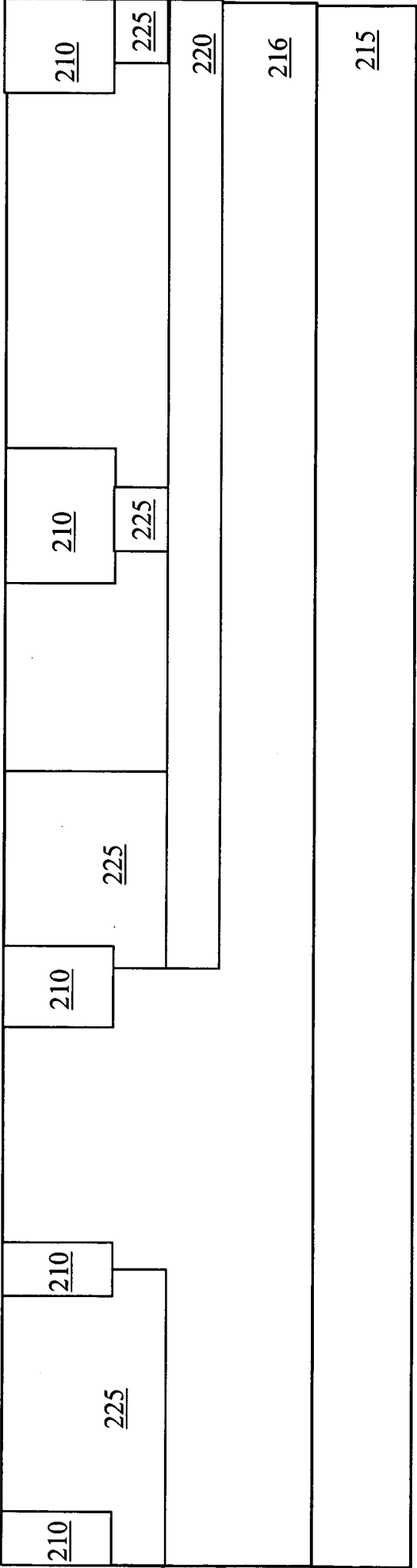


FIGURE 4

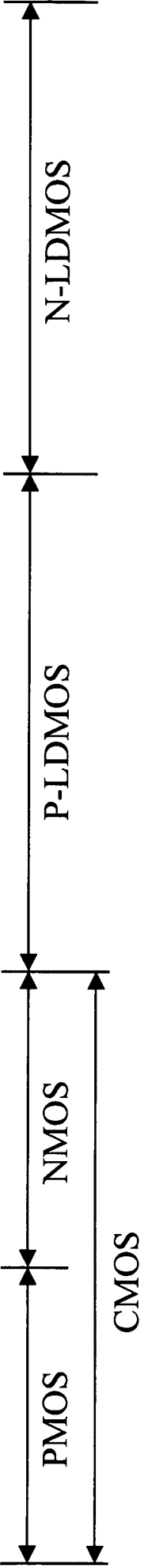
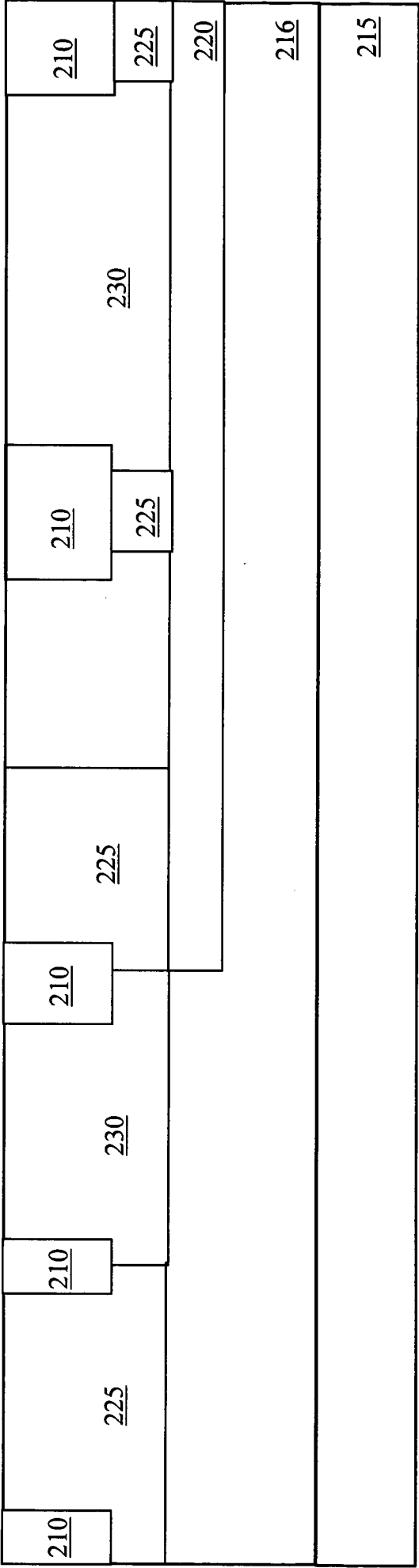


FIGURE 5

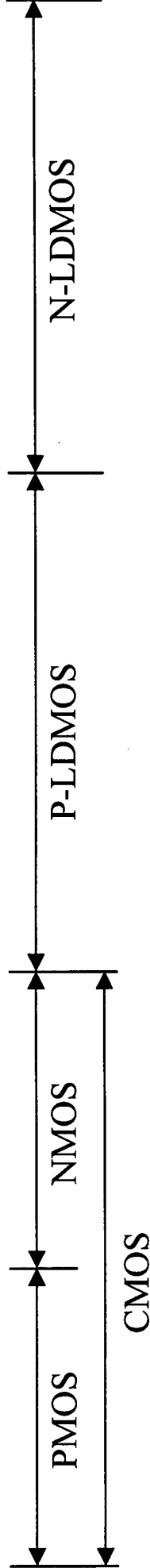
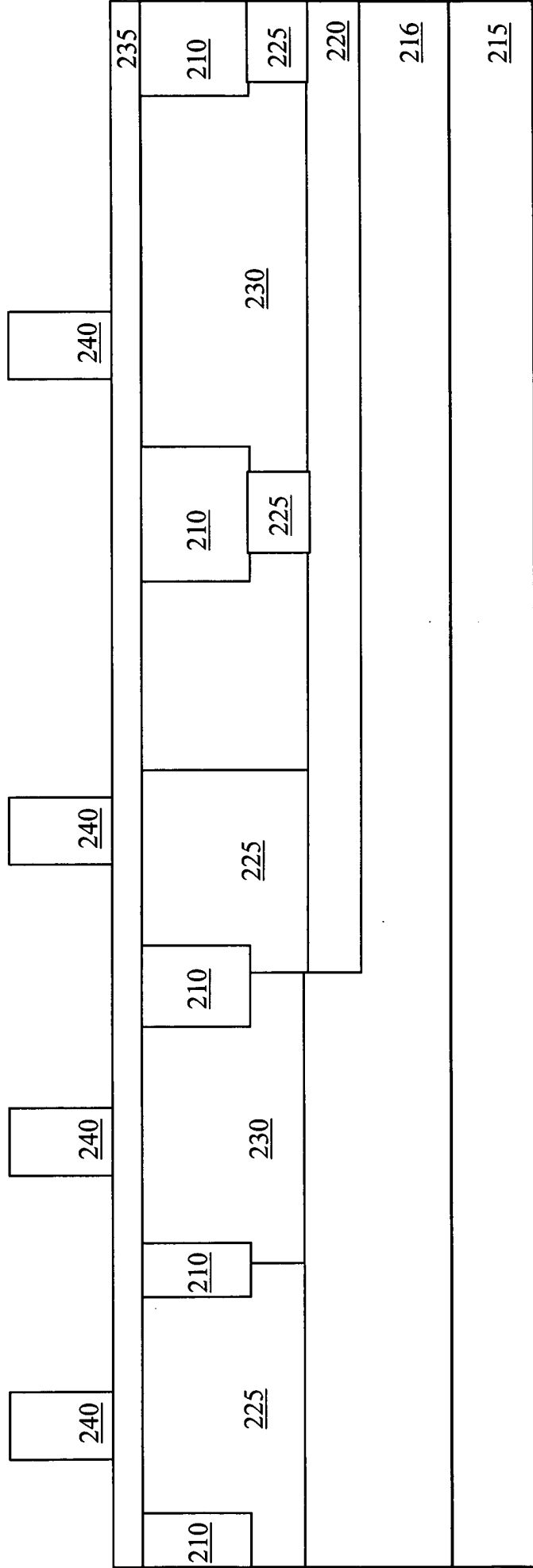


FIGURE 6

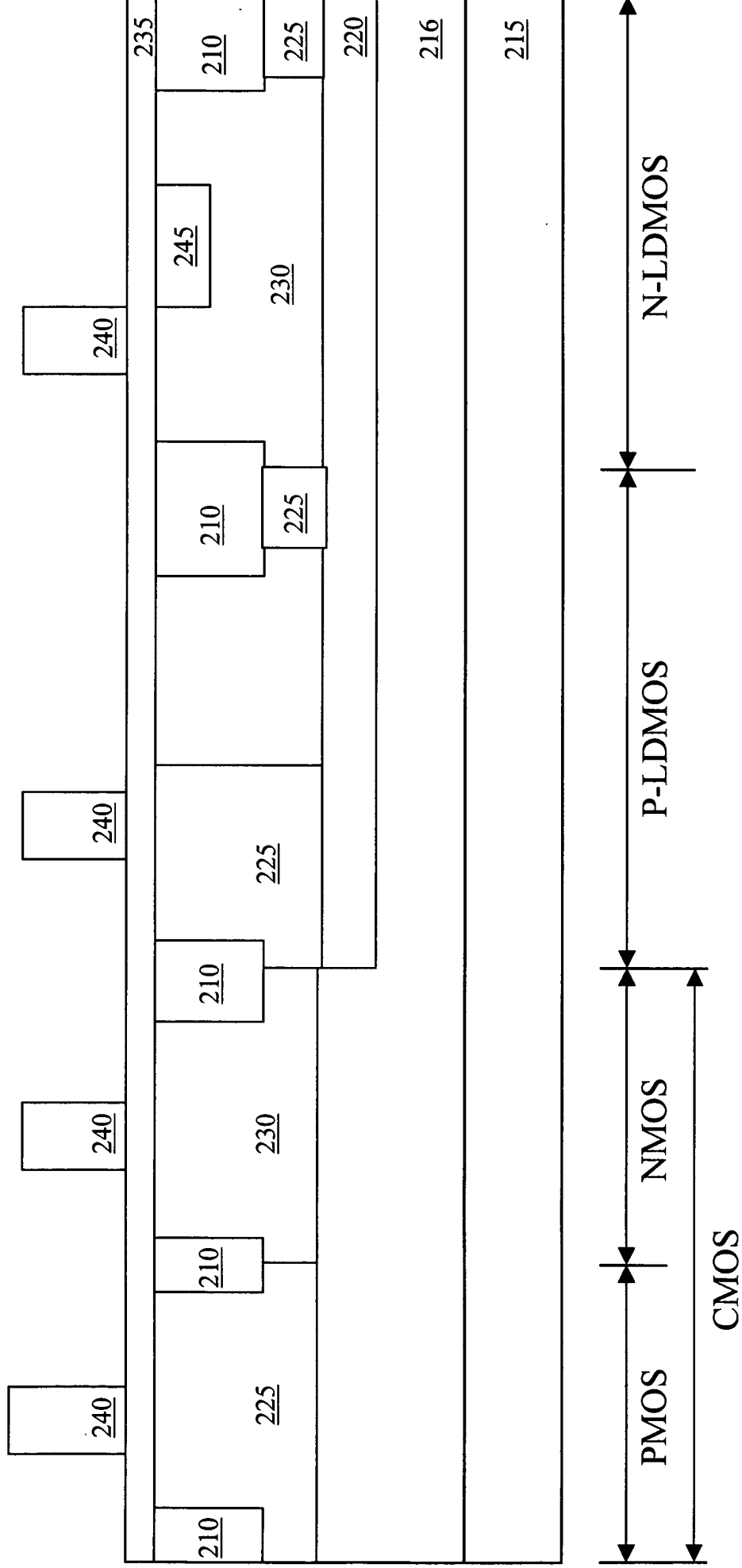


FIGURE 7

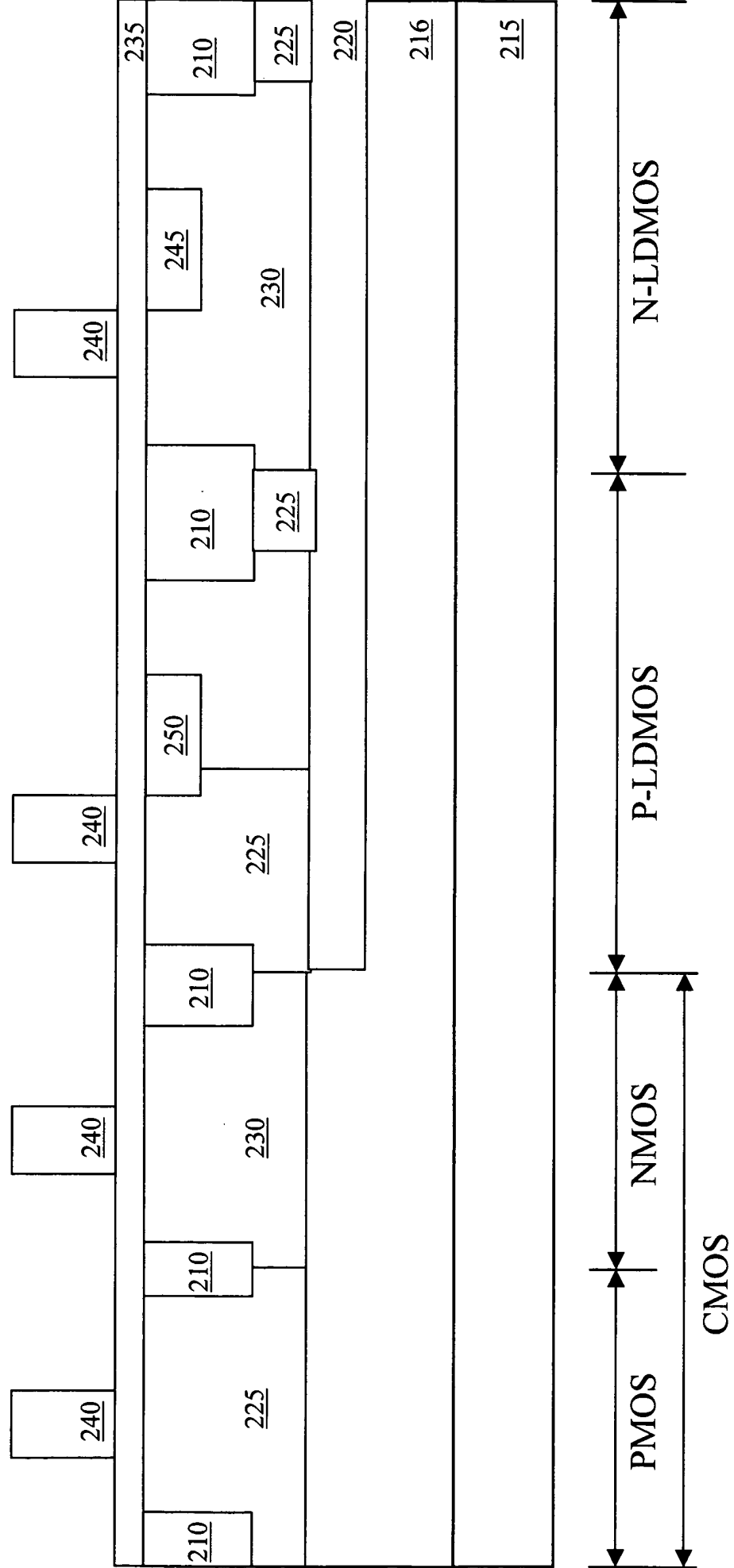


FIGURE 8



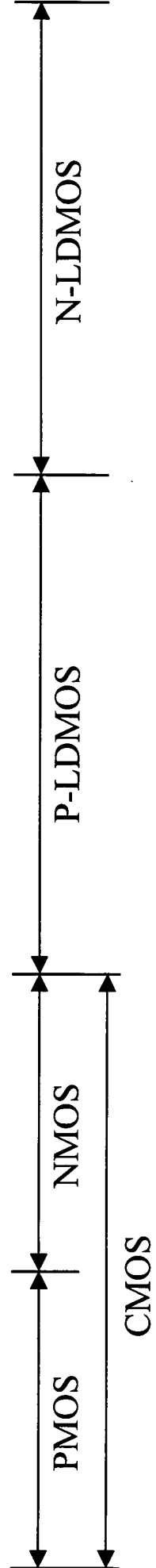
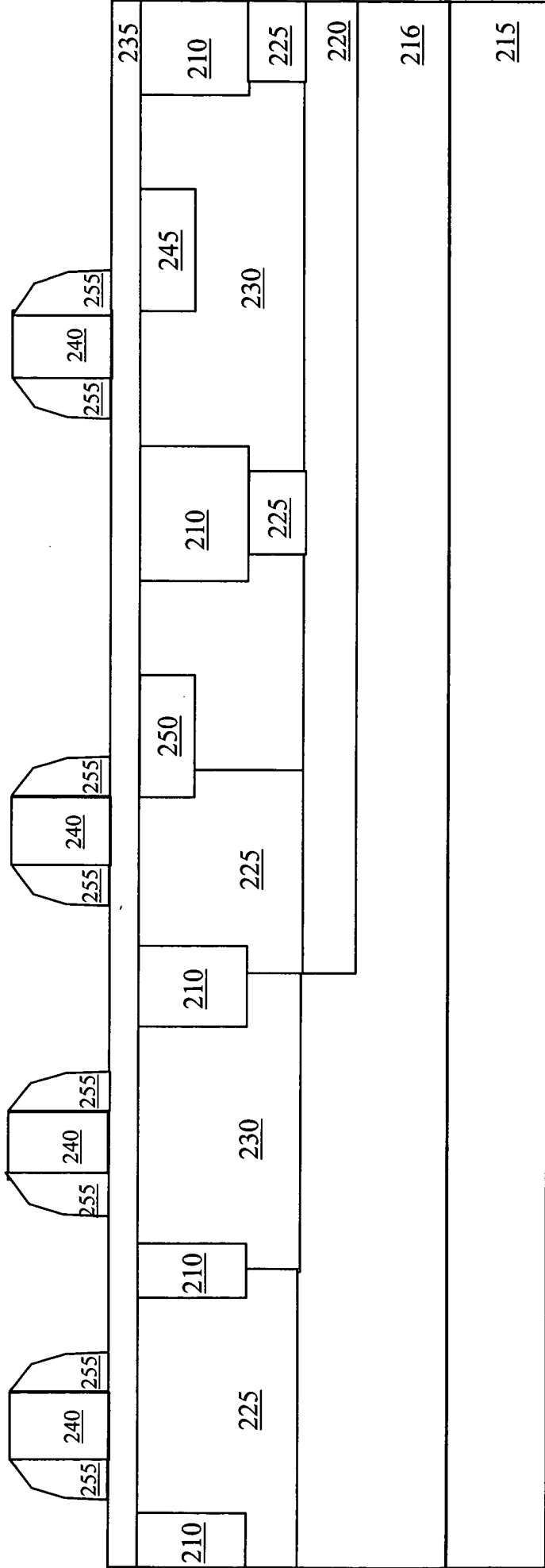


FIGURE 9

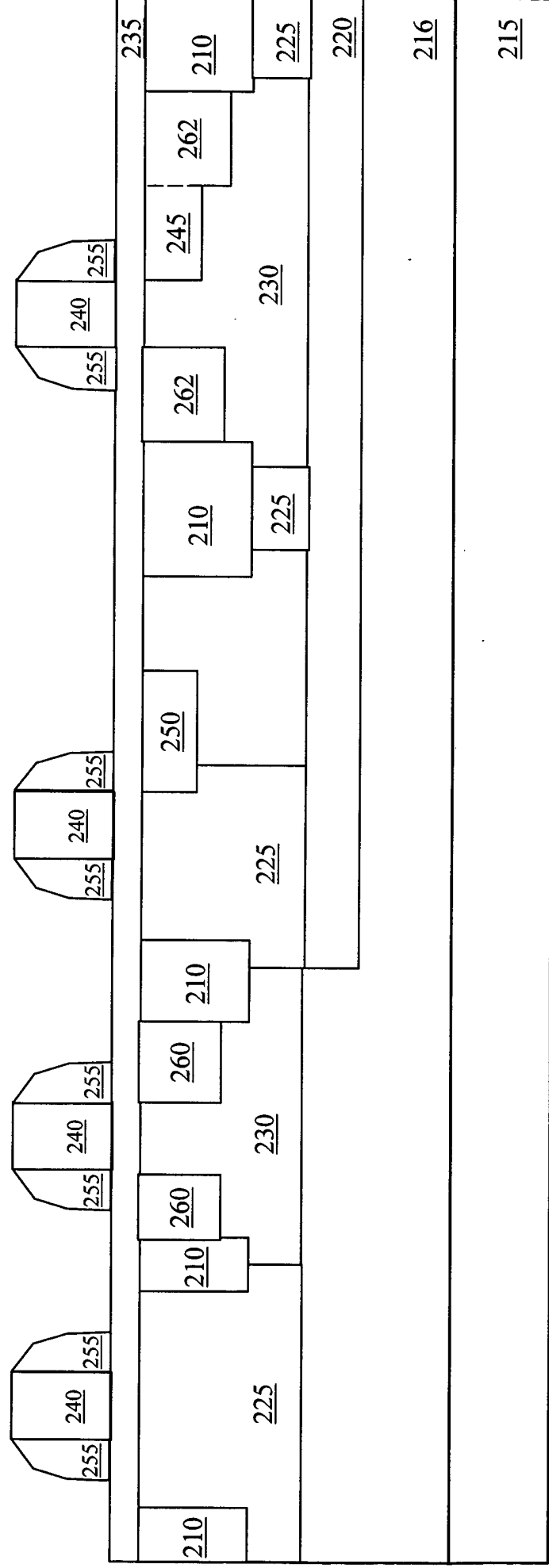


FIGURE 10

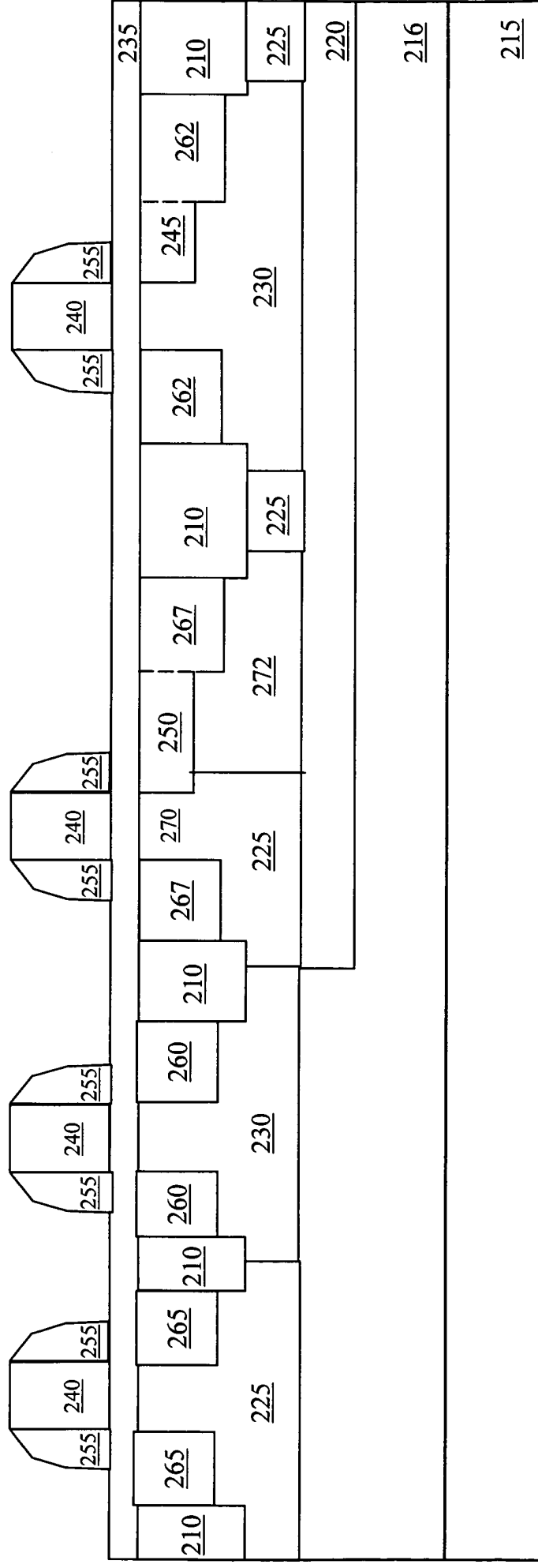


FIGURE 11

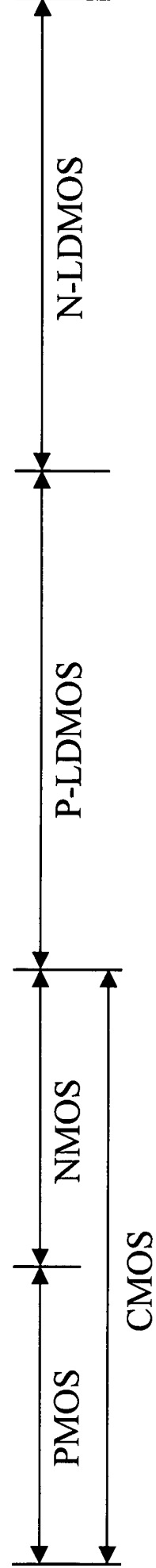
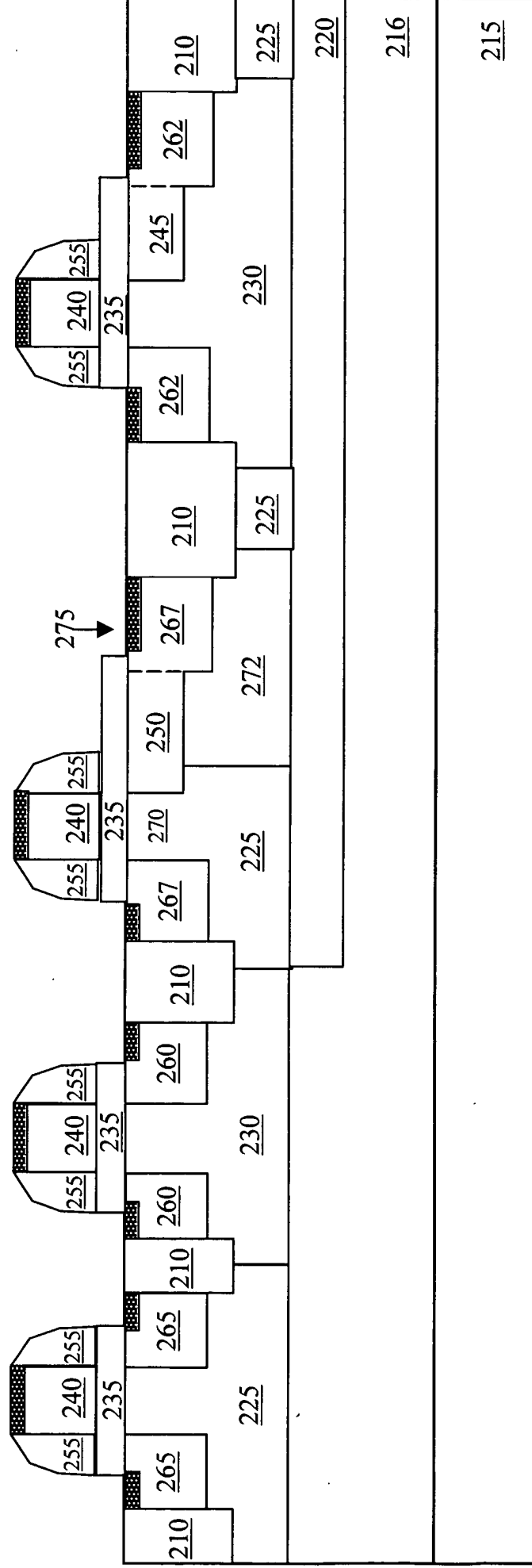


FIGURE 12

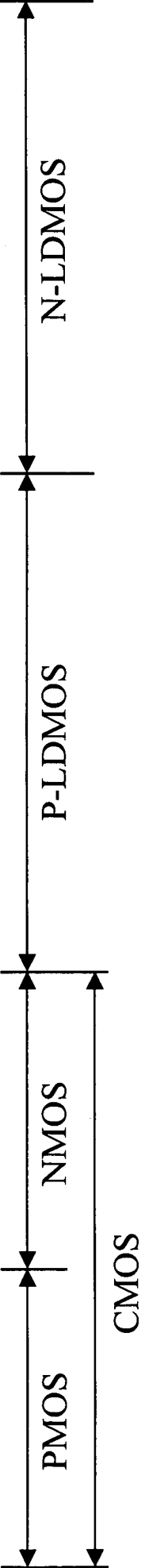
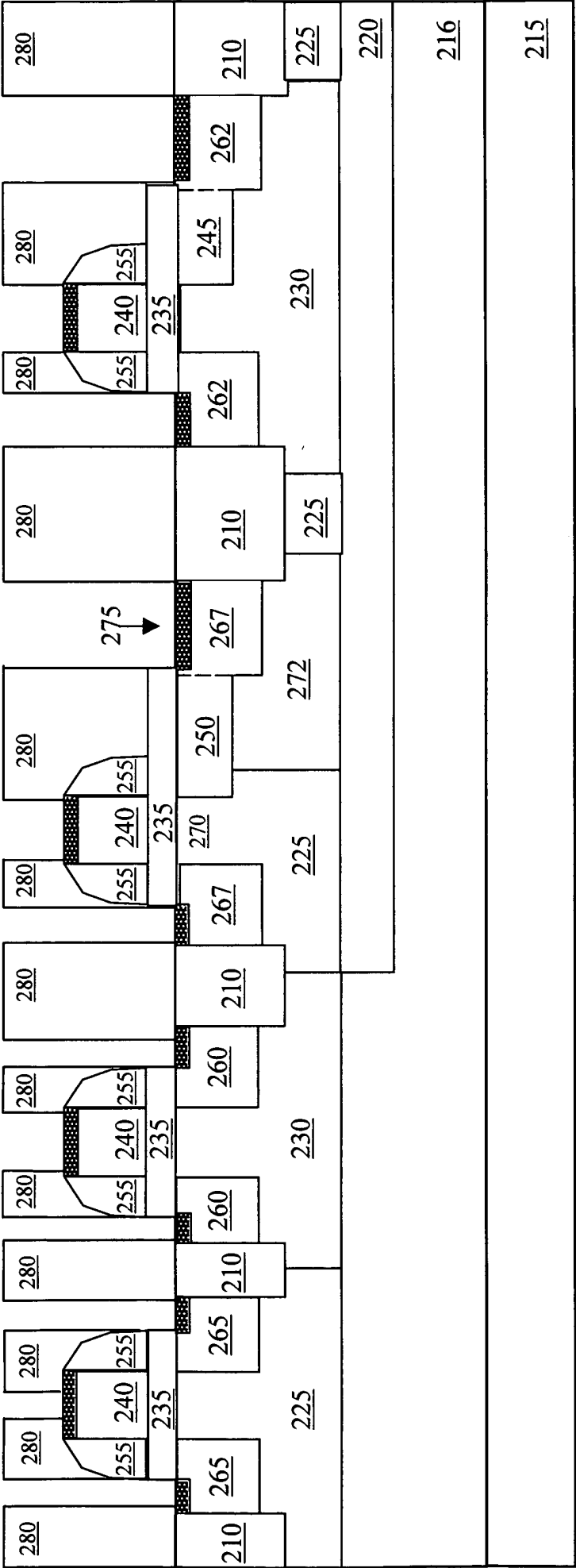


FIGURE 13

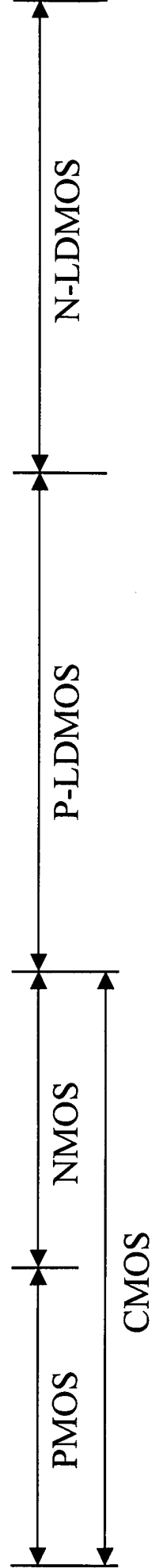
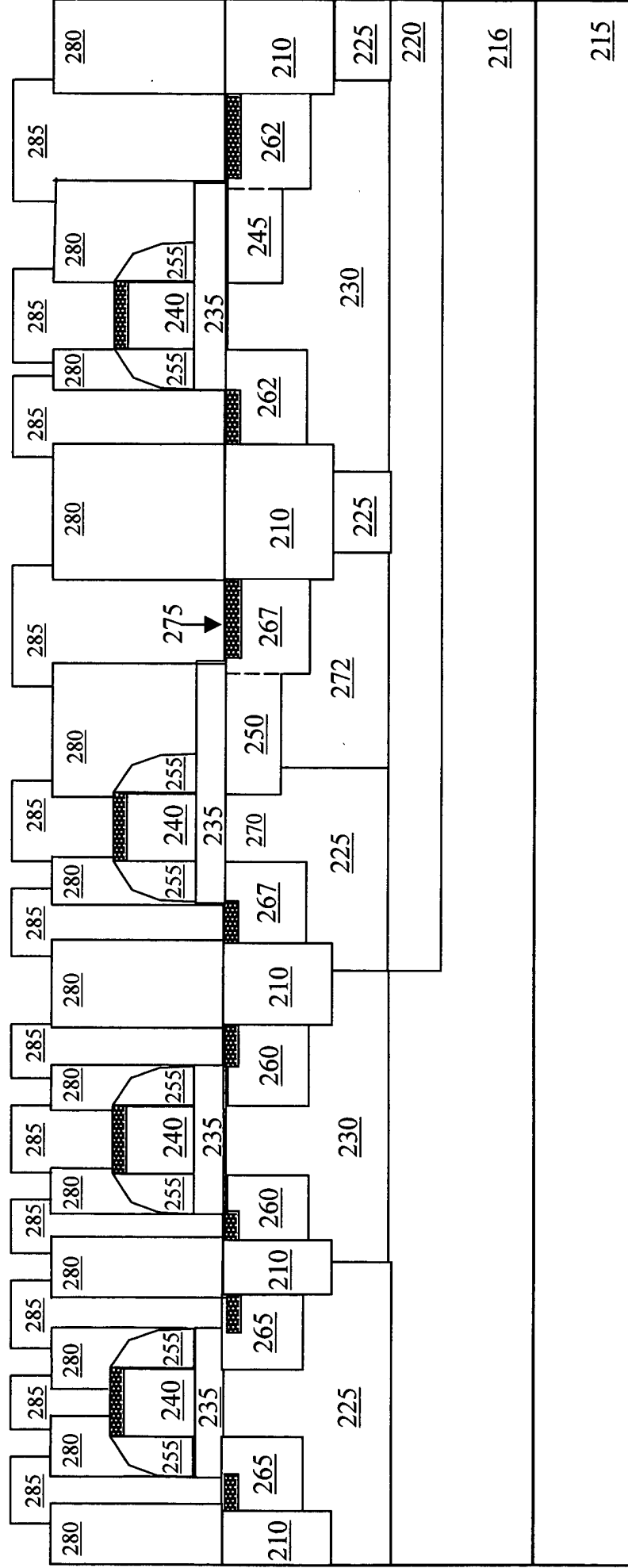
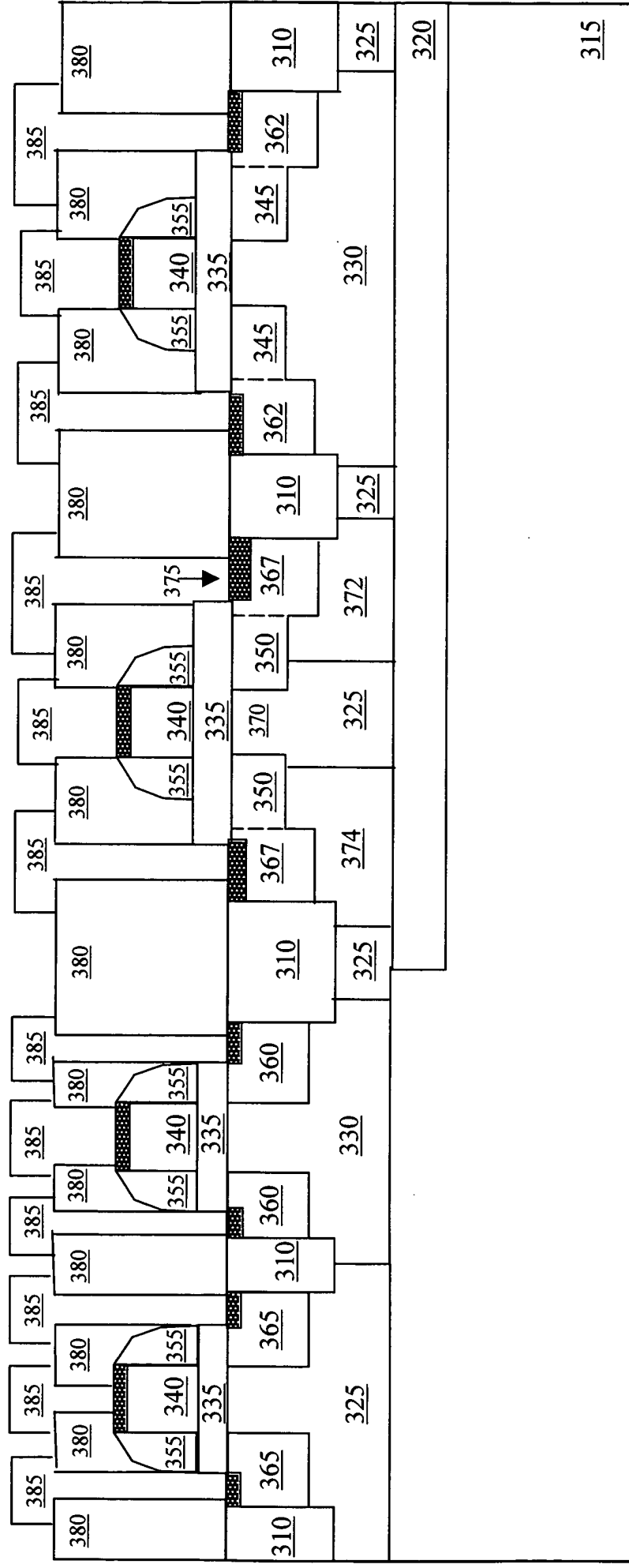


FIGURE 14



PMOS

NMOS

P-LDMOS

N-LDMOS

CMOS

FIGURE 15